Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims:

1-24.   (Canceled)

25. (Previously Presented)   A system comprising:

a plurality of storage devices; and

a control unit for controlling read/write of data requested by a plurality of host processors into the plurality of storage devices, using a plurality of logical storage areas constituted by storage areas of the plurality of storage devices,

wherein, when the control unit receives multiple requests from different host processors for the same logical storage area, the multiple requests are handled in parallel.

26.   (Previously Presented) The system according to claim 25, wherein the control unit includes a plurality of host adaptors which control data transfer between the control unit and the plurality of storage devices.

27.   (Previously Presented) The system according to claim 25, wherein the control unit includes a plurality of disk

adaptors which control the read/write of data from/to the

plurality of logical storage areas.

28.   (Previously Presented) The system according to claim

26, wherein the control unit includes a plurality of disk

adaptors which control the read/write of data from/to the

plurality of logical storage areas.

29.   (Previously Presented) The system according to claim

28, wherein the control unit includes cache memories which

enable the transfer of data between the host adaptors and the

disk adaptors.

30.   (Previously Presented) The system according to claim

25, wherein the control unit includes a control memory which

stores control information into a plurality of tables.

31. (Previously Presented) The system according to claim

25, wherein the control unit permits the multiple requests to

be handled in parallel if the data ranges of the multiple

requests do not overlap in the same logical storage area.

32. (Currently Amended) A system comprising:

a plurality of storage devices; and

a control unit for controlling read/write of data

requested by a plurality of host processors into the plurality

of storage devices, using a plurality of logical storage areas

constituted by storage areas of the plurality of storage

devices,

wherein, when the control unit receives multiple

requests from different host processors for the same logical

storage area and determines ifthat the data ranges of the

multiple requests overlap, the multiple requests are not

handled in parallel.

33. (Previously Presented) The system according to claim

32, wherein the control unit includes a plurality of host

adaptors which control data transfer between the control unit

and the plurality of storage devices.

34. (Previously Presented) The system according to claim

32, wherein the control unit includes a plurality of disk

adaptors which control the read/write of data from/to the

plurality of logical storage areas.

35.   (Previously Presented) The system according to claim 33, wherein the control unit includes a plurality of disk adaptors which control the read/write of data from/to the plurality of logical storage areas.

36.   (Previously Presented) The system according to claim 35, wherein the control unit includes cache memories which enable the transfer of data between the host adaptors and the disk adaptors.

37.   (Previously Presented) The system according to claim 32, wherein the control unit includes a control memory which stores control information into a plurality of tables.